

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	47367	motion with (detect\$3 sens\$3) & switch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 14:48
L2	118	1 & (timer with reset\$3 with (motion near12 (sens\$3 detect\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 14:50
L3	58	2 & batter\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 14:50
L4	50	3 & (automatically automatic\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 15:20
L5	14	3 & ((automatically automatic\$3) with switch\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 14:51
L6	464490	5 & (microprocessor processor controller microcontroller) same tim\$3 samse reset\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 14:59
L7	12	5 & (microprocessor processor controller microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 15:22
L8	24783	1 & (microprocessor processor controller microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 15:20

L9	467	8 & ((automatically automatic\$3) with battery)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 15:38
L10	171	9 & (tim\$3 with reset\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 15:24
L11	61	9 & (tim\$3 with reset\$3 with (sens\$3 detect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 15:25
L12	23	11 & (tim\$3 with control\$3 with switch\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 15:39
L13	20	12 & (tim\$3 with interval)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 15:38
L14	110	8 & ((automatically automatic\$3) with (open\$3 off disconnect\$3) with battery)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 15:39
L15	26	14 & (tim\$3 with control\$3 with switch\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 15:40
S1	113621	motion with (detect\$3 sens\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 14:48

S2	2183932	microprocessor processor controller microcontroller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:03
S3	982118	(microprocessor processor controller microcontroller) same tim\$3 samse reset\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 14:58
S4	32557	S1 & S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:04
S5	2818526	switch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:04
S6	21775	S4 & S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:05
S7	7147	S6 & batter\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:06
S8	313	S7 & (pcb (prit\$3 near3 circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:07
S9	1268	S7 & (pcb (print\$3 near3 circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:11

S10	198	S9 & (batter\$3 with terminal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:12
S11	47	S10 & ((motion detect\$3 sens\$3) with reset\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:21
S12	19	S11 & ((transistor switch\$3) with reset\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:32
S13	6	S12 & (automatical\$3 with (open\$3 off disconnect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:37
S14	14	S11 & (automatical\$3 with (open\$3 off disconnect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 11:41
S15	39	S10 & (automatical\$3 with (open\$3 off disconnect\$3 reset\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 12:15
S16	2	"20050088120"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 12:15
S17	1	S16 & (support\$3 with bas\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/26 12:15